IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Manrique J. Brenes; Yen T. Nguyen		
Assignee:	Cisco Technology, Inc.		
Title:	METHOD AND SYSTEM FOR REAL-TIME BIT ERROR RATIO DETERMINATION		
Serial No.:	10/773,705	Filing Date:	February 6, 2004
Examiner:	Stephen M. Baker	Group Art Unit:	2112
Docket No.:	CIS0209US	Confirmation No.	8310

Austin, Texas
January 11, 2008

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P. O. BOX 1450
ALEXANDRIA, VA 22313-1450

RESPONSE TO NON-FINAL OFFICE ACTION

Dear Sir:

This paper is being submitted in response to the Office Action dated September 11, 2007, having a shortened statutory period set to expire on December 11, 2007. Accompanying this response is a one-month Petition Under 37 C.F.R. § 1.136 for Extension of Time setting a new time for response of January 11, 2008. Further examination and reconsideration are respectfully requested in view of the following amendments and remarks.

No Amendments to the Specification are presented in this paper.

No Amendments to the Drawings are presented in this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.